Text Editor

Source Code "RTL"

Constraints Files

Standard Cell Libary

*.lib

*.v

yosys — Synthesis

Netlist

"importer"

"placer"

"router"

Standard Cell Libary

*.alf

Hierarchic Layout

"clock tree generator"

"test pattern generator" — *.bsdl → Test Pattern File

"design rule checker" → Report File

"exporter" — *.gds2 *.cif → Export File

"static timing analyzer" → Report File

QtFlow proposal